

FIG. 1a.

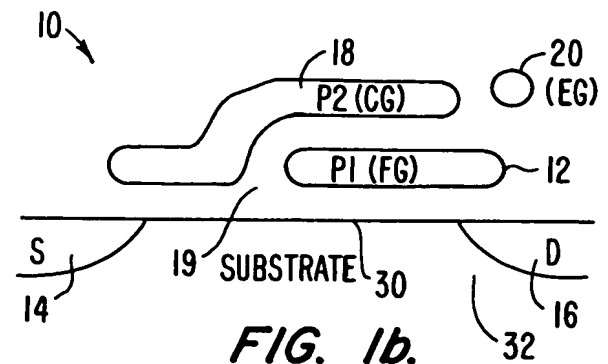


FIG. 1b.

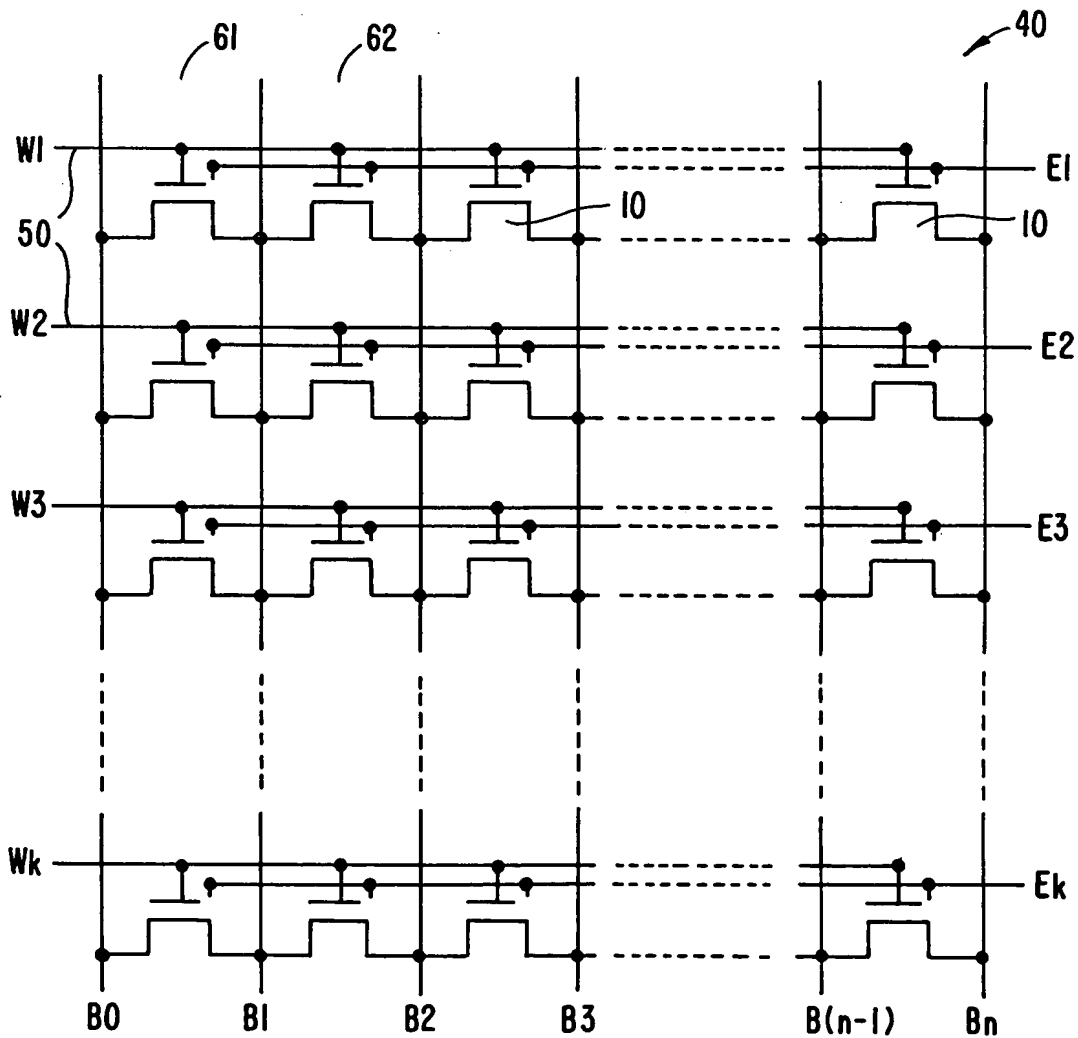


FIG. 2.



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VOLTAGE CONDITIONS FOR NORMAL MEMORY OPERATIONS

TYPE OF LINES	READ	PROGRAM	ERASE
SELECTED WORD LINE (WL)	5V	12V	0V
NON-SELECTED WORD LINE (WL)	0V	0V	0V
SELECTED SOURCE LINE (SL)	0V	0V	0V
SELECTED DRAIN LINE (DL)	1.5V	8V	0V
NON-SELECTED BIT LINES (BL)	FLOAT	FLOAT	0V
SELECTED ERASE LINE (EL)	0V	0V	20V
NON-SELECTED ERASE LINE (EL)	0V	0V	0V

FIG. 3.

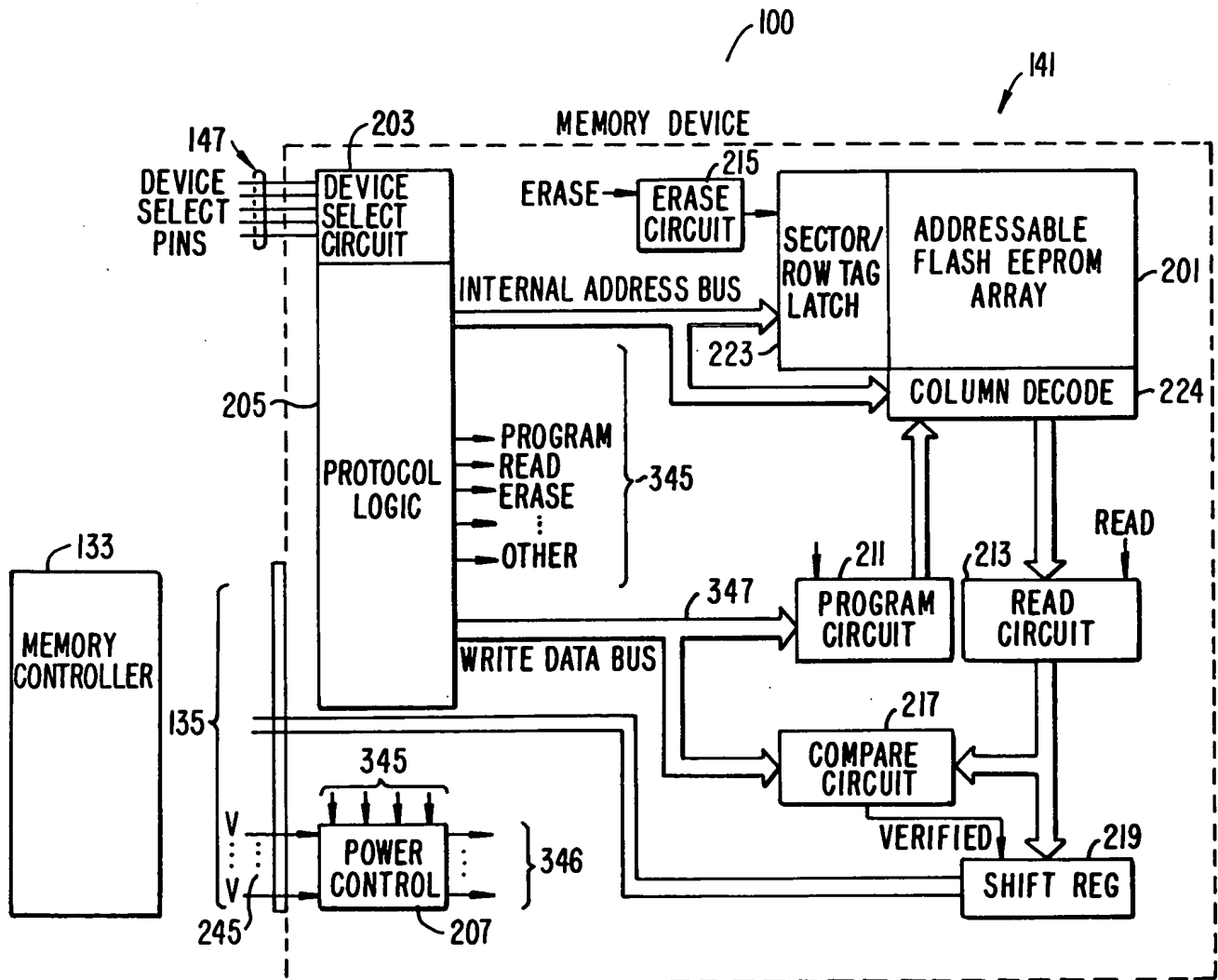


FIG. 4.

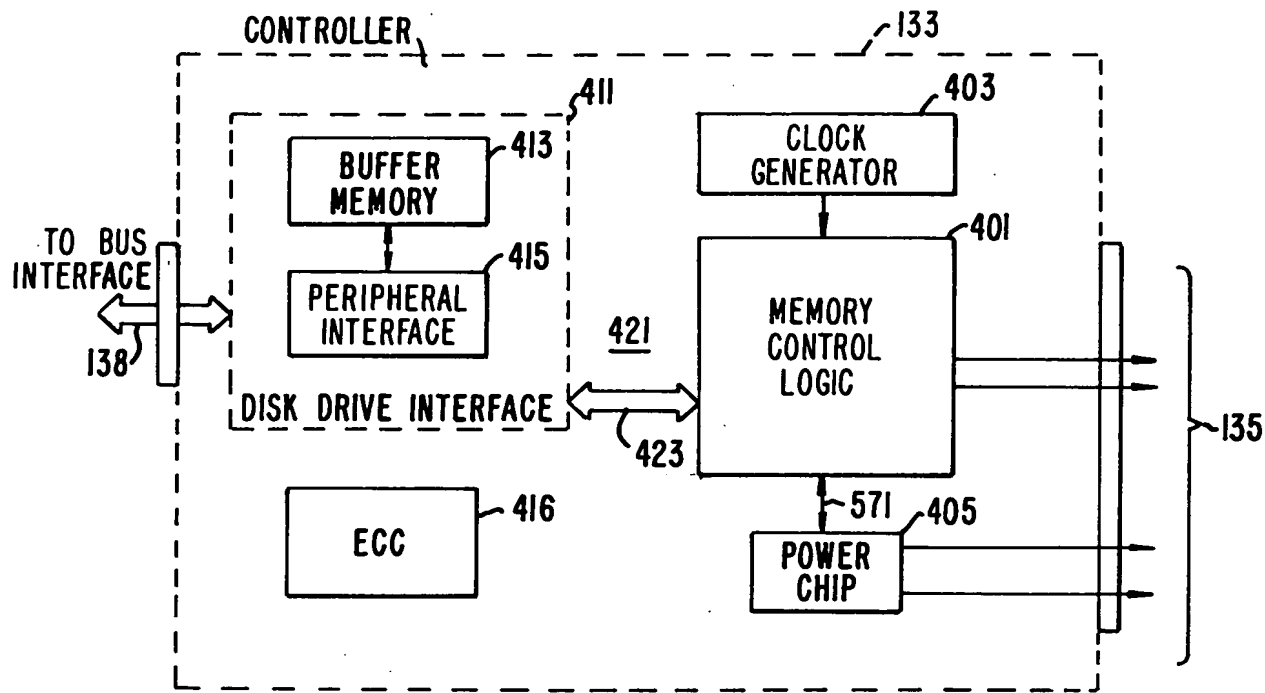


FIG. 5.

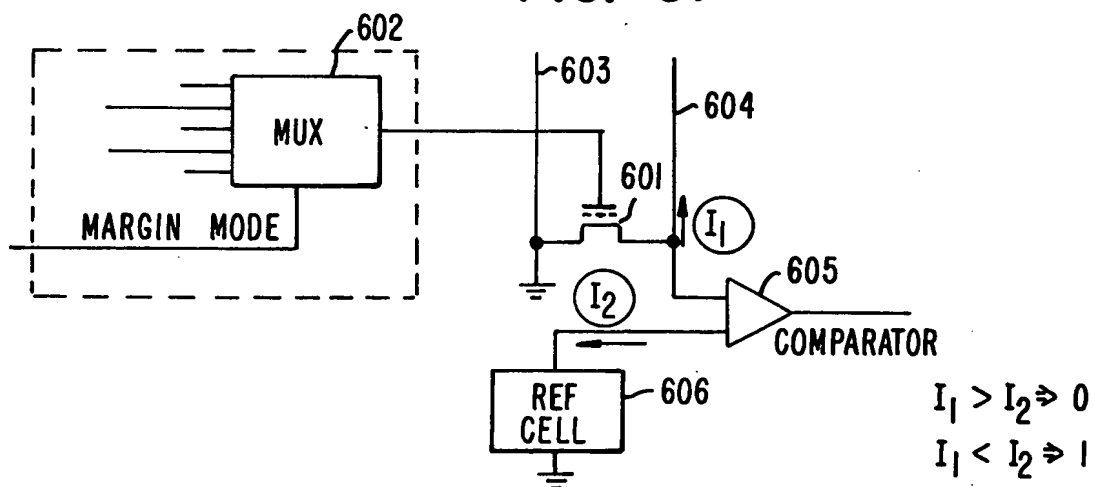


FIG. 6a.

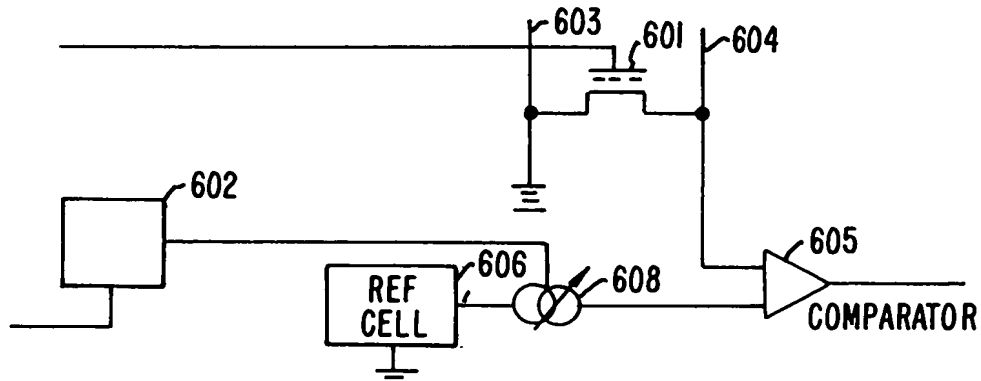


FIG. 6b.

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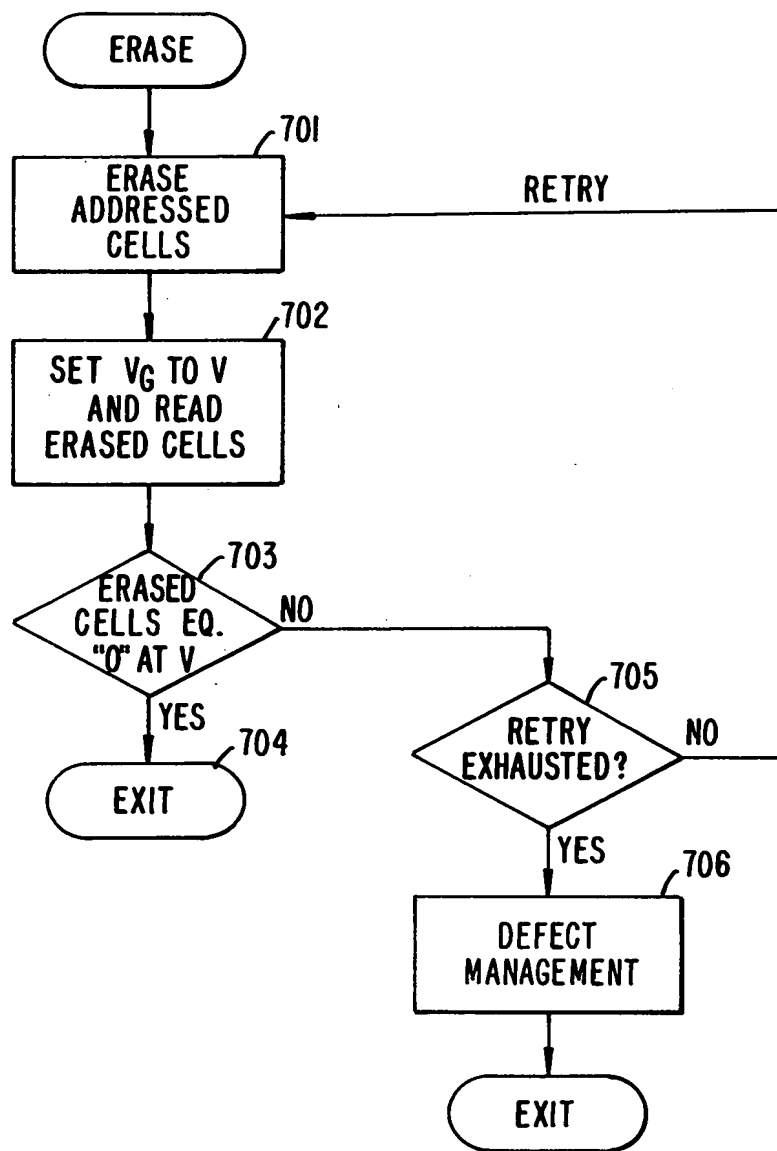


FIG. 7.

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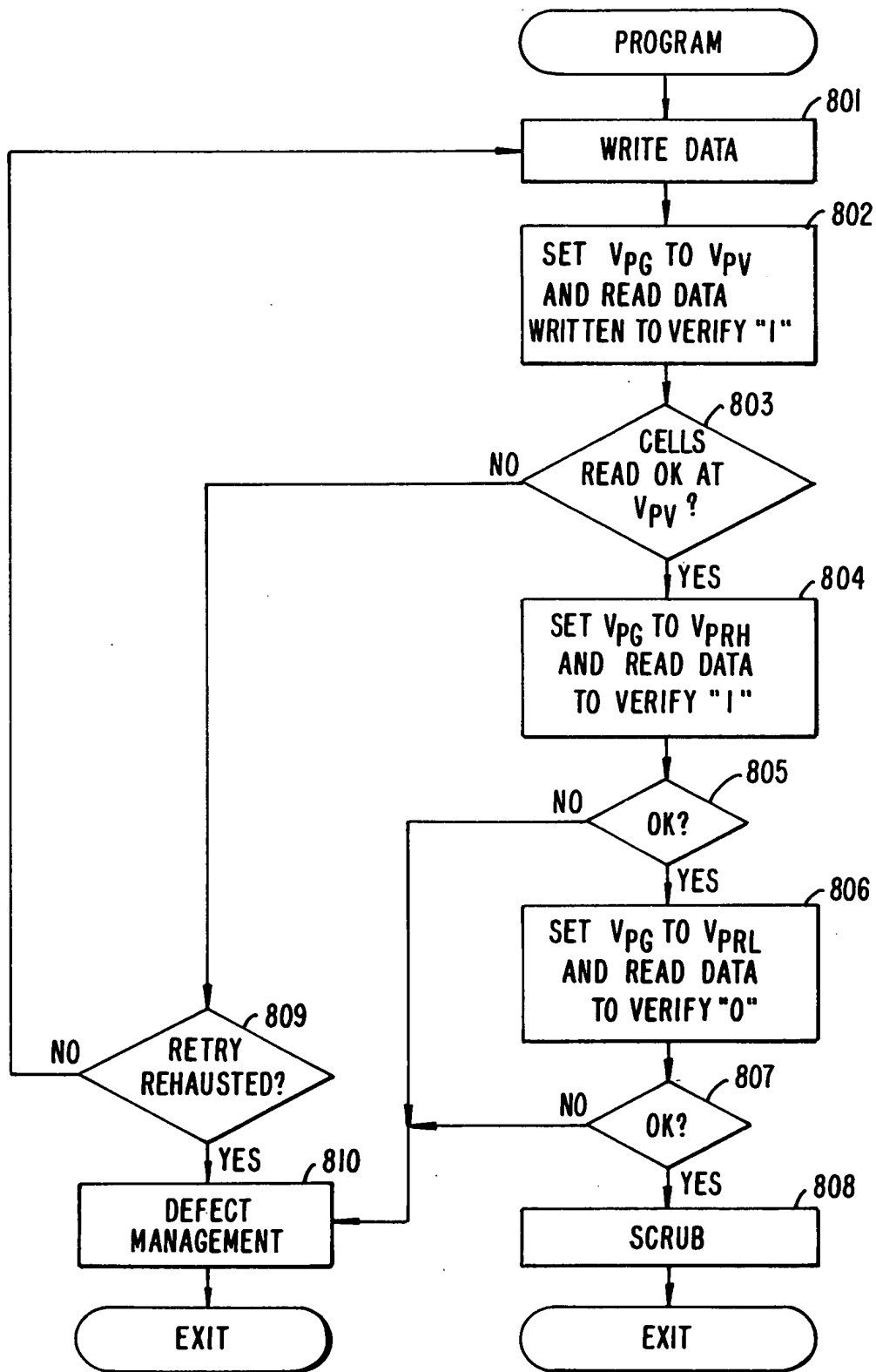


FIG. 8.

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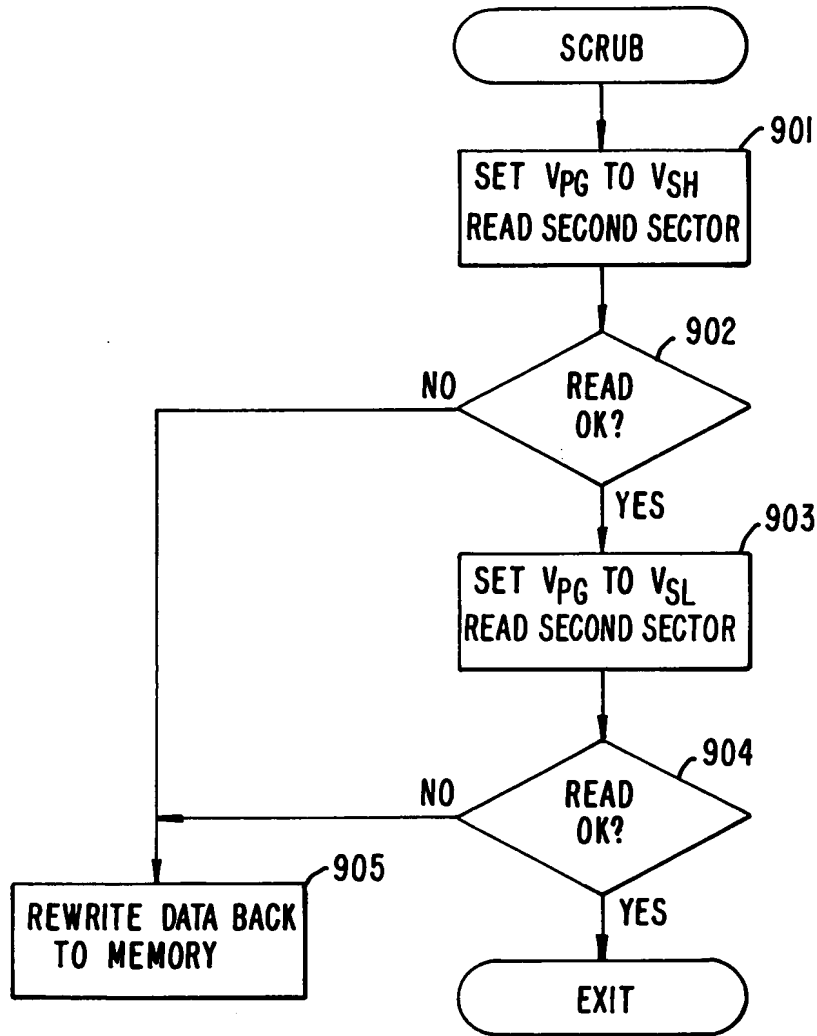
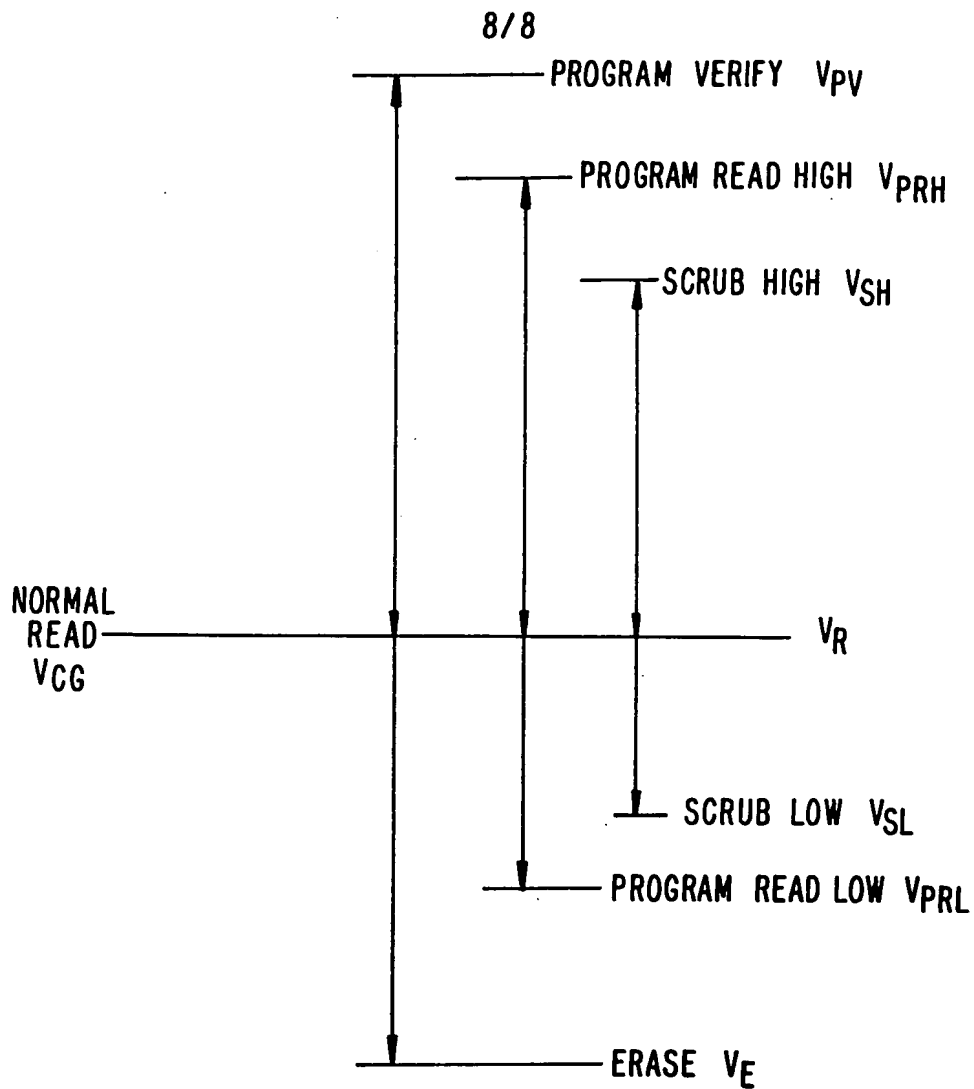


FIG. 9.



**FIG. 10.**

$V_N$	6.4
$V_{PRH}$	5.6
$V_{SH}$	5.2
$V_R$	5.0
$V_{SL}$	4.4
$V_{PRL}$	4.2
$V_E$	3.8

**FIG. 11.**